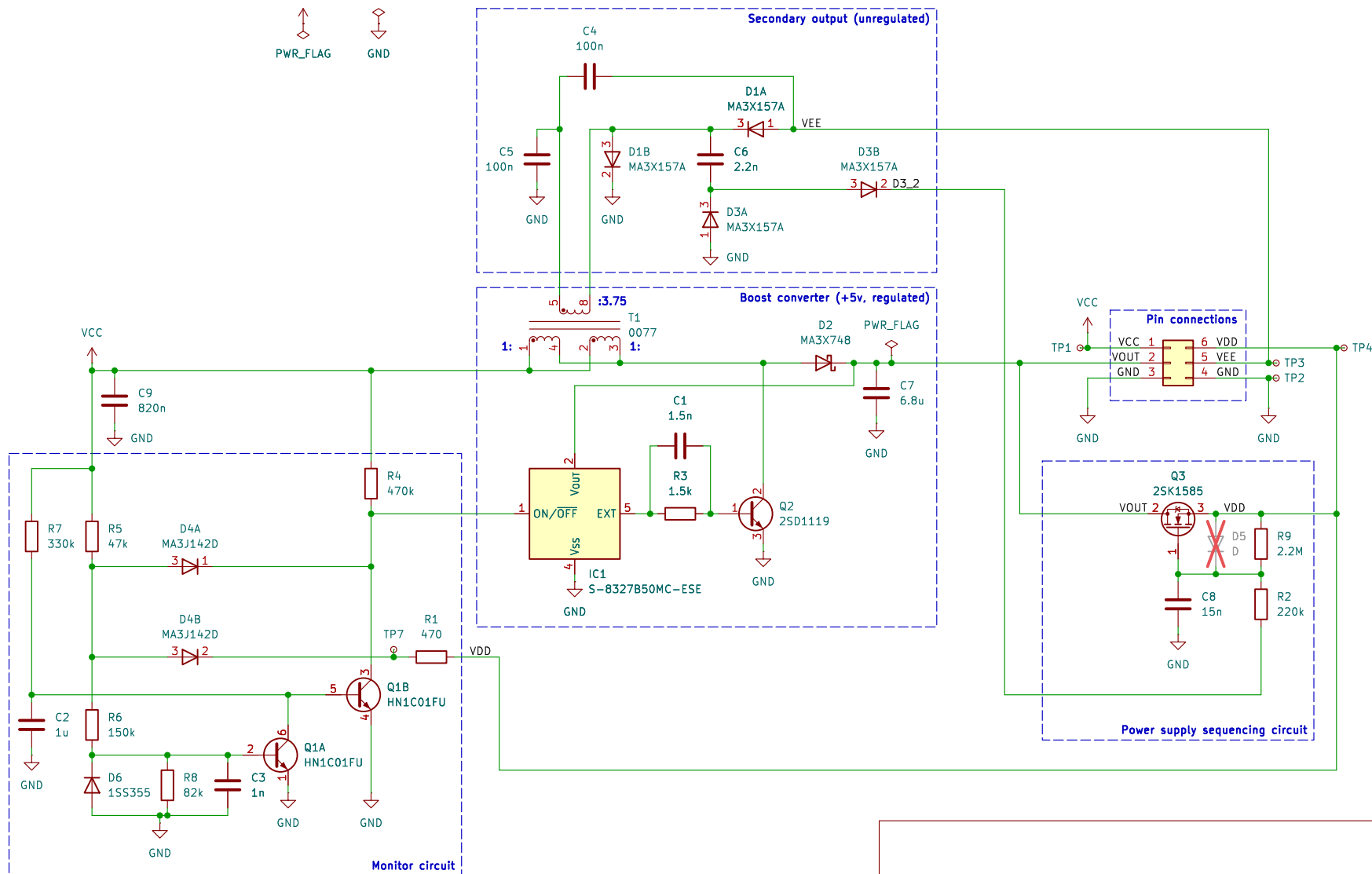


VCC
PWR_FLAG
GND

Power nets:
VDD = +5V regulated positive output
VEE = unregulated negative output, roughly -18V
VCC = DC input supply (battery or DC jack), nominal +3V
VOUT = raw +5V output before power supply sequencing



<https://github.com/gekkio/gb-schematics>
<https://gekkio.fi>

Sheet: /
File: LSEP01120A1.kicad_sch

Title: LSEP01120A1

Size: A4
KiCad E.D.A. 8.0.2

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